

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	1557	340/635	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/06 12:29
S2	53	(over near2 (voltage current frequency)) same (record\$3 stor\$3) same ((specified predetermined preset particular) near5 (condition\$1 specification))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/06 12:39
S3	1	S1 and S2	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/06 12:32
S4	115914	"324"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/06 12:33
S5	3	S2 and S4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/06 12:33
S6	39	S2 and (stor\$3 record\$3) near5 (memory storage medium media)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/06 12:40
S7	16	S6 and (indicating display) near6 (condition\$1 number\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/06 12:41
S8	59	("3790910").URPN.	USPAT	OR	ON	2005/09/06 15:59
S9	59	("3790910").URPN.	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/09/06 15:59
S10	103	(over near2 (voltage current frequency)) same ((record\$3 stor\$3) with (display\$3 indicat\$3) near5 (number\$1 time\$1))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/09/06 16:02
S11	4	S10 and (compar\$5 near5 (predetermined reference\$1 preset threshold\$1)) same ((record\$3 stor\$3) near4 (memory storage volatile indelible))	US-PGPUB; USPAT; EPO; JPO	OR	ON	2005/09/06 16:03
S12	13	("5751532").URPN.	USPAT	OR	ON	2005/09/06 16:26
S13	4	((over adj2 voltage) near8 (integrated adj circuit)) with ((stor\$3 record\$3) near5 (memory storage))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/06 16:31

S14	2	((("4845632") or ("6044476")).PN.	US-PGPUB; USPAT	OR	OFF	2005/09/06 16:31
S15	31	("3735201"   "4240149"   "4272816"   "4321681"   "4331997"   "4362986"   "4528611"   "4530024"   "4550360"   "4556882"   "4587590"   "4620156"   "4626772"   "4631625"   "4670812"   "4685024"   "4722059"   "4751605"   "4755903"   "4937769"   "4996624"   "4998098"   "5041737"   "5140492"   "5157575"   "5208545"   "5262679"   "5309312"   "5317472"   "5325061"   "5367426").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/06 18:05
S16	2839	340/657-664.ccls.	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/06 18:06
S17	65	S16 and (compar\$4 near6 (threshold\$1 predetermined preset\$3 reference\$1)) with (stor\$3 record\$3 memory)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/06 18:07
S18	7	S17 and (IC or processor or microprocessor or semiconductor) same condition\$1	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/06 18:13
S19	1	S17 and (IC or processor or microprocessor or semiconductor) same (over adj2 (voltage current speed))	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/06 18:16
S20	33	S17 and (IC or processor or microprocessor or semiconductor)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/06 18:16
S21	0	S17 and (IC or processor or microprocessor or semiconductor) near5 (over near4 (heat temperature voltage current))	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/06 18:17
S22	371133	S17 and (IC or processor or microprocessor or semiconductor) wiht (over near4 (heat temperature voltage current))	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/06 18:17
S23	1	S17 and (IC or processor or microprocessor or semiconductor) with (over near4 (heat temperature voltage current))	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/06 18:39
S24	1212653	(indelible near2 memory storage)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/06 18:40
S25	26	(indelible near2 (memory storage))	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/16 22:14

S26	1	S25 and (over near3 (voltage current)) near5 (record\$3 stor\$3)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/06 18:45
S27	1	S25 and (over near3 (voltage current))	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/06 18:41
S28	1	S25 and (condition\$1) near5 (record\$3 stor\$3)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/06 18:41
S29	1735	(over near3 (voltage current)) near5 (record\$3 stor\$3)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/06 18:46
S30	118	(over near3 (voltage current)) near5 (record\$3 stor\$3) same (disk CD tape)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/06 18:46
S31	0	(over near3 (voltage current)) near5 (record\$3 stor\$3) with (number near5 occur\$3) same (disk CD tape)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/06 18:47
S32	6	(over near3 (voltage current)) near5 (record\$3 stor\$3) with (number\$1) same (disk CD tape)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/06 18:49
S33	0	S29 and (324/759)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/06 18:50
S34	191	(324/759)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/06 18:50
S35	0	S34 and S29	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/06 18:50
S36	7	S34 and (voltage current) near5 (detect\$3 sens\$3) same (record\$3 stor\$3)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/06 18:58
S37	2839	340/662,663,664,661,657-660.ccls.	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/06 18:58
S38	9187	((over or under) near4 (voltage current)) same (record\$3 stor\$3) same (display indicat\$3)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/06 18:59
S39	60	S37 and S38	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/06 18:59
S40	9	S39 and (compar\$4 near4 (threshold predetermined reference))	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/06 19:12
S41	0	S40 and (specif\$3 particular) near5 (number near3 (record\$3 stor\$3))	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/06 19:04

S42	1	S40 and (specif\$3 particular) near5 ((record\$3 stor\$3))	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/06 19:04
S43	0	340/653 and (over near3 voltage) same (record\$3 with counter)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/06 19:13
S44	1	340/653 and (over near3 voltage) same (record\$3 )	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/06 19:13
S45	3	("5796256").URPN.	USPAT	OR	ON	2005/09/06 19:15
S46	8781	324/500,522,537,755,763,765,771, 759.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/09/06 19:16
S47	0	(out near4 specification) near5 (detect\$3 sense\$3 monitor\$3) same (record\$3 with indicat\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/09/06 19:17
S48	11	(over near3 voltage) near5 (detect\$3 sense\$3 monitor\$3) same (record\$3 with indicat\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/09/06 19:17
S49	3	("5796256").URPN.	USPAT	OR	ON	2005/09/06 20:03
S50	2	("4119926"   "4319298").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/07 11:02
S51	2	("6246332").URPN.	USPAT	OR	ON	2005/09/07 11:04
S52	3	("5796256").URPN.	USPAT	OR	ON	2005/09/07 11:30
S53	5	("4636724"   "4823088"   "5083117"   "5796256"   "6049213").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/07 11:46
S54	886	(IC or semiconductor) same (over near3 (voltage current)) same (sens\$3 detect\$3 monitor\$3) same (indicat\$3 display record\$3 stor\$3)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/07 11:48
S55	0	((IC or semiconductor) same (over near3 (voltage current)) same (sens\$3 detect\$3 monitor\$3) same (indicat\$3 display record\$3 stor\$3)).ti.	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/07 11:48
S56	4	((IC or semiconductor) same (over near3 (voltage current)) same (sens\$3 detect\$3 monitor\$3) same (indicat\$3 display record\$3 stor\$3)).ab.	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/07 11:56

S57	12	((IC or semiconductor) same (over adj2 (voltage current)) same (indicat\$3 display record\$3 stor\$3)).ab.	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/07 11:57
S58	3	S56 not S57	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/07 11:57
S59	11	S57 not S56	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/07 11:57
S60	20	("20010047216"   "20020082787"   "20020120417"   "20030052401"   "4833586"   "5097421"   "5323440"   "5455409"   "5586250"   "5656915"   "5720767"   "5760492"   "5805438"   "5835700"   "5847950"   "5867809"   "5995774"   "6031749"   "6188206"   "6392376").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/07 12:03
S61	1	("2003/0052401").URPN.	USPAT	OR	ON	2005/09/07 13:16
S62	20	("20010047216"   "20020082787"   "20020120417"   "20030052401"   "4833586"   "5097421"   "5323440"   "5455409"   "5586250"   "5656915"   "5720767"   "5760492"   "5805438"   "5835700"   "5847950"   "5867809"   "5995774"   "6031749"   "6188206"   "6392376").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/07 13:16
S63	1	"5021828".PN.	USPAT; USOCR	OR	ON	2005/09/07 13:18
S64	53	("5021828").URPN.	USPAT	OR	ON	2005/09/07 13:28
S65	0	S64 and (out near3 specification) same (record\$3 near7 (memory storage))	USPAT	OR	ON	2005/09/07 13:29
S66	0	S64 and (out near3 spec\$5) same (record\$3 near7 (memory storage))	USPAT	OR	ON	2005/09/07 13:29
S67	0	S64 and (over near4 (voltage current)) same (record\$3 near7 (memory storage))	USPAT	OR	ON	2005/09/07 13:30
S68	0	S64 and (number near5 use\$2) same (record\$3 near7 (memory storage))	USPAT	OR	ON	2005/09/07 13:30
S69	53	("5021828").URPN.	USPAT	OR	ON	2005/09/07 13:34
S70	963	(memory storage) near4 (fuse\$1 near4 blow\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/07 13:35

S71	0	S70 and (indelible same (record\$3 stor\$3 log\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/07 13:35
S72	234	S70 and ((abnormal\$3 over) same (record\$3 stor\$3 log\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/07 13:36
S73	13	S70 and ((abnormal\$3 or (over adj2 (voltage current))) same (record\$3 stor\$3 log\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/08 09:45
S74	3	("6204768"   "6438502"   "6581022").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/07 13:46
S75	0	("6754607").URPN.	USPAT	OR	ON	2005/09/07 14:29
S76	1	(history near2 (information data record\$3 stor\$3)) with (over adj3 voltage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/09/07 14:34
S77	1908	((information data record\$3 stor\$3)) with (over adj3 voltage)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/09/07 14:35
S78	380	((information data record\$3 stor\$3)) with (over adj3 voltage).ab.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/09/07 14:35
S79	1	((information data record\$3 stor\$3) with (over adj3 voltage) same (read adj only adj memory)).ab.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/09/07 14:37
S80	7	((information data record\$3 stor\$3) with (over adj3 voltage) same (read adj only adj memory))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/09/07 14:44
S81	26	((information data record\$3 stor\$3) with (clock adj2 speed) same (read adj only adj memory))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/09/07 14:49
S82	0	((information data record\$3 stor\$3) with (monitor\$3 near5 (abnormal near5 (clock adj2 speed))) same (read adj only adj memory))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/09/07 14:51

S83	0	((monitor\$3 near5 (abnormal near5 (clock adj2 speed)))) same (read adj only adj memory))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/09/07 14:51
S84	0	(monitor\$3 near5 (abnormal near5 (clock adj2 speed)))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/09/07 14:53
S85	7346	(computer or processor) same ((voltage clock speed temperature current) near5 (monitor\$3 sens\$3 detect\$3) with (record\$3 stor\$3 history))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/09/07 14:55
S86	9	(computer or processor) same ((voltage same clock same speed) same (monitor\$3 sens\$3 detect\$3) same (record\$3 stor\$3 history)).ab.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/09/07 15:01
S87	2234	(computer or processor) and ((voltage same clock same speed) same (monitor\$3 sens\$3 detect\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/09/07 15:06
S88	154711	"340"/\$.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/09/07 15:02
S89	110	S87 and S88	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/09/07 15:02
S90	0	S89 and (record\$3 stor\$3) same (abnormal near3 operation)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/09/07 15:04
S91	5	S89 and (record\$3 stor\$3) same (measur\$3 near7 operation)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/09/07 15:05
S92	735	(computer or processor) same ((voltage same clock same speed) same (monitor\$3 sens\$3 detect\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/09/07 15:06

S93	43	S88 and S92	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/09/07 15:06
S94	1	S93 and (over near3 voltage) and (clock near2 speed) and (record\$3 stor\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/09/07 15:08
S95	21	(over near3 voltage) same (clock near2 speed) same (record\$3 stor\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/09/08 09:26
S96	0	(computer IC semiconductor) same ((over near3 voltage) with (clock adj speed) with (record\$3 stor\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/09/07 15:12
S97	8	702/117 and (over near2 voltage) same (record\$3 stor\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/09/07 15:13
S98	0	"364"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/08 09:18
S99	721	701/22	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/08 09:18
S100	0	"364"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/08 09:19
S101	1368	"364"/552	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/08 09:19
S102	2089	S99 or S101	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/08 09:19
S103	0	S102 and (over near2 voltage) same ((reord\$3 stor\$3) near5 (memory))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/08 09:20



S10 4	0	S102 and (out near specification) same ((reord\$3 stor\$3) near5 (memory))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/08 09:20
S10 5	44	S102 and (voltage near4 (sens\$3 detect\$3 monitor\$3)) same ((reord\$3 stor\$3) near5 (memory))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/08 09:21
S10 6	0	S102 and (voltage near4 (sens\$3 detect\$3 monitor\$3)) same ((reord\$3 stor\$3) near5 (memory)) same ((count\$1 accumulat\$3 increment\$3) near3 number)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/08 09:22
S10 7	44	S102 and (voltage near4 (sens\$3 detect\$3 monitor\$3)) same ((reord\$3 stor\$3) near5 (memory))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/08 09:22
S10 8	13	(computer IC microprocessor) same (over near3 voltage) same (clock near2 speed) same (record\$3 stor\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT	OR	ON	2005/09/08 09:27
S10 9	2	(indelible near3 memory) and (fuse\$1)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/08 09:46
S11 0	2	("5680353").URPN.	USPAT	OR	ON	2005/09/08 09:47
S11 1	5	("4055802"   "4217637"   "4759123"   "4942450"   "4998223").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/08 09:48
S11 2	53	(over near2 (voltage current frequency)) same (record\$3 stor\$3) same ((specified predetermined preset particular) near5 (condition\$1 specification))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/08 09:57
S11 3	18	(indelible near2 (memory storage)) and (count\$3 occur\$3 triggered)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/16 22:15
S11 4	2	(indelible near2 (memory storage)) and (count\$3 occur\$3 triggered) with (memory)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/16 22:15
S11 5	14	("4559637"   "4639293"   "4665497"   "4682287"   "4710888"   "4831438"   "5521815"   "5602987"   "5646994"   "5654891"   "5774374"   "5799083"   "5815093"   "5828751").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/16 22:33
S11 6	0	(integrate near2 circuit) same (memory with (PROM or ROM)) same (fuse\$1)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/16 22:34

S11 7	0	((integrate near2 circuit) same ((PROM or ROM)) same (fuse\$1))	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/16 22:35
S11 8	1150	((integrate near2 circuit) or (IC or semiconductor\$1)) same ((PROM or ROM)) same (fuse\$1)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/16 22:35
S11 9	341	((integrate near2 circuit) or (IC or semiconductor\$1)) same ((PROM or ROM)) near8 (fuse\$1)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/16 22:36
S12 0	3	S119 and (store\$4 record\$3) near6 (memory with count\$3)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/17 01:57
S12 1	7	(microprocessor semiconductor IC) near6 (over near4 voltage) near8 (sens\$3 detect\$3 measur\$3) same (record\$3 stor\$4)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/17 02:02
S12 2	0	(microprocessor semiconductor IC) near6 (over adj2 voltage) near8 (sens\$3 detect\$3 measur\$3) same (record\$3 stor\$4)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/17 02:02
S12 3	15	(microprocessor semiconductor IC) same (over adj2 voltage) near5 (sens\$3 detect\$3 measur\$3) same (record\$3 stor\$4)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/17 02:07
S12 4	2386	(microprocessor semiconductor IC) same (over adj2 voltage)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/17 02:08
S12 5	319	(microprocessor semiconductor IC) same ((over adj2 voltage) with (detect\$3 sens\$3 measur\$4))	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/17 02:08
S12 6	165	(microprocessor semiconductor IC) with ((over adj2 voltage) with (detect\$3 sens\$3 measur\$4))	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/17 02:09
S12 7	38	(microprocessor semiconductor IC) with ((over adj2 voltage) with (detect\$3 sens\$3 measur\$4)) same (record\$3 stor\$4)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/17 02:30
S12 8	46979	(EEPROM) near4 (memory)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/17 02:31
S12 9	12	(EEPROM) near4 (memory) and (indelible)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/17 02:39
S13 0	0	(mori near3 kazuhisa).in. and (over near2 voltage)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/17 02:39
S13 1	4	(mori near3 kazuhisa).in. and (over near2 current)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/17 02:40

S13 2	0	(integrated adj circuit) with (over-voltage or over adj voltage) with (measur\$4)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/17 02:41
S13 3	29	(integrated adj circuit) with (over-voltage or over adj voltage) with (measur\$4 detect\$4 sens\$4)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/17 02:46
S13 4	77	(microprocessor or processor or CPU) with (over-voltage or over adj voltage) with (monitor\$3 measur\$4 detect\$4 sens\$4)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/17 02:46
S13 5	51	(microprocessor or processor or CPU) with (over-voltage or over adj voltage) near6 (monitor\$3 measur\$4 detect\$4 sens\$4)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/17 02:47
S13 6	0	S135 and (over-voltage or (over adj voltage)) near6 (memory)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/17 02:47
S13 7	1	S135 and (over-voltage or (over adj voltage)) with (memory)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/17 02:48
S13 8	0	S135 and (over-voltage or (over adj voltage)) with (record\$3 storage)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/17 02:48
S13 9	51	S135 and (over-voltage or (over adj voltage))	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/17 02:48
S14 0	1	S135 and (over-voltage or (over adj voltage)) same (prevent\$3 near4 damage)	US-PGPUB; USPAT; USOCR	OR	ON	2005/09/17 04:10